

Chenyuan Zhou

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EDUCATION

Shanghai Jiao Tong University

2023.9 - 2027.6(expected)

B.S in computer science, **ACM Honors Class**

GPA(Core Courses): **3.94/4.3**

Selected courses:

- Algorithm Design and Analysis :**A+**, **98/100**
- Compiler Design and Implementation:**A+**, **99/100**
- Computer Architecture:**A+**, **97/100**
- Data Structure:**A+**, **97/100**
- Mathematical Analysis(Honor):**A+**, **95/100**

RESEARCH INTEREST

My research interests are Computer Vision and Multimodal Learning, especially visual representation learning and multimodal perception for real-world understanding.

RESEARCH EXPERIENCE

RHOS (Yong-Lu Li, Ce-Wu Lu)

2025.6 - now

Conducted experiments and participated in research on Multimodal Learning under the supervision of Yong-Lu Li and Ce-Wu Lu.

PROJECTS

RISCV CPU (Course Project)

Fall 2024 [Link to Repo](#)

Designed a CPU in Verilog implementing the basic RISCV instruction set.

- Implemented branch prediction and instruction prefetching.
- Successfully executed on FPGA.

Mx Compiler (Course Project)

Summer 2024 [Link to Repo](#)

A compiler for Mx* (an educational language with basic features of C), as well as Clang (with `mem2reg` and register allocation).

- Implemented various optimizations including SCCP, DCE, inlining, GVN & GCM, loop detection, and unrolling.
- Utilized SSA graph coloring to allocate registers based on liveness analysis.

AWARDS AND HONORS

2023, 2024 Zhiyuan Honors Scholarship (2 % in SJTU)

STUDENT WORK AND TEACHING EXPERIENCE

Computer Programming, Teaching Assistant	2024 Fall
Data Structure, Teaching Assistant	2025 Spring
Principle and Practice of Computer Algorithms, Teaching Assistant	2025 Summer
Vice Monitor of ACM Honors Class 2024	2024.9 - 2025.9

SKILLS

Programming	C++, Python, Java, Verilog,
Tools	Git, Solidworkers, L ^A T _E X
Languages	Chinese(Native speaker), English(Fluent)